AMENDMENTS TO THE CLAIMS

The listing below replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (previously presented) A system for driving rows of a liquid crystal display comprising:

at least one module for driving one single row of said liquid crystal display, said module comprising an inverter operating in a supply path between a first and a second supply line of said system, said first supply line comprising a first switch for coupling a first power terminal of the inverter to a first or to a second supply voltage and said second supply line comprising a second switch for coupling a second power terminal of the inverter to a third or to a fourth supply voltage, said inverter being driven by logic circuitry and providing a drive signal for one single row of said liquid crystal display.

2. (Previously presented) The system according to claim 1, wherein said inverter comprises a PMOS transistor and a NMOS transistor.

3. (Previously presented) The system according to claim 1, wherein the value of said first supply voltage exceeds said second supply voltage, the value of said second supply voltage exceeds said third supply voltage, and the value of said third supply voltage exceeds said fourth supply voltage.

4. (Currently amended) The system according to claim 1, wherein said first and second ~~means~~ switches are controlled by a logic signal that controls respectively the connection of the first supply line to said first or to said second supply voltage and the connection of the second supply line to said third or to said fourth supply voltage according to whether a frame is uneven or even.

5. (Previously presented) The system according to claim 4, wherein said logic circuitry comprises a logic device capable of supplying an additional input logic signal to an elevator device capable of raising the level of said additional logic signal for driving said inverter.

6. (Previously presented) A module for driving a row in a liquid crystal display comprising:

an inverter having first and second power terminals;

a first switch for coupling the first power terminal of the inverter to a first or a second supply voltage; and

a second switch for coupling the second power terminal of the inverter to a third or fourth supply voltage, wherein the inverter is driven by a logic circuit and provides a drive signal for the row.

7. (Previously presented) The module of claim 6, wherein the inverter comprises a PMOS transistor and a NMOS transistor.

8. (Previously presented) The module of claim 6, wherein the first and second supply voltages have different values, and the third and fourth supply voltages have different values.

9. (Previously presented) The module of claim 6, wherein the first and second switches are driven by a logic signal, the state of the logic signal being determined by whether a frame is uneven or even.

10. (Previously presented) The module of claim 9, further comprising a level shifter.